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Docket: 0756-2395

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
HONGYONG ZHANG)
Serial No. 09/987,607) Group Art Unit: 2813
Filed: 11/15/2001) Examiner: D. Hogans
For: METHOD FOR PRODUCING)
SEMICONDUCTOR INTEGRATED)
CIRCUIT INCLUDING A THIN FILM)
TRANSISTOR AND A CAPACITOR)

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

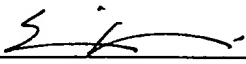
In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 9-20-02


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